FIG. 1

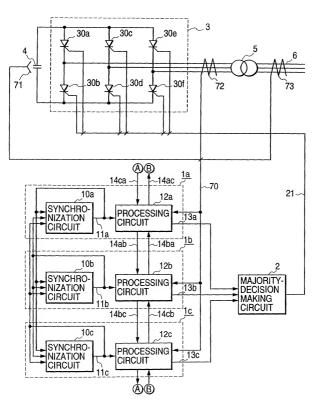


FIG. 2

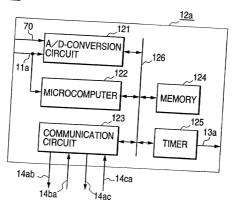


FIG. 4

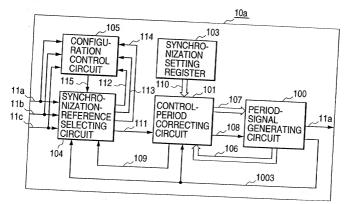


FIG. 3(a)

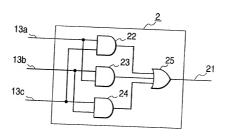
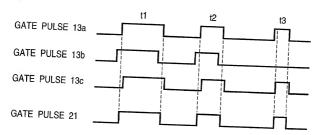


FIG. 3(b)





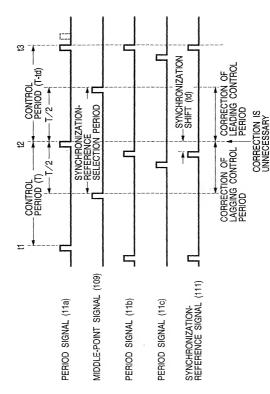


FIG. 6

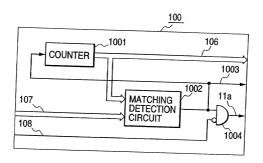


FIG. 7

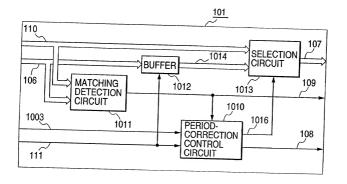
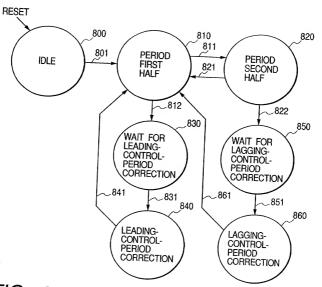
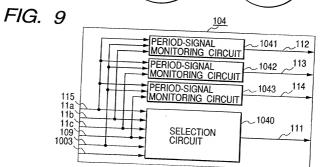


FIG. 8





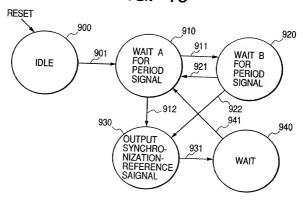
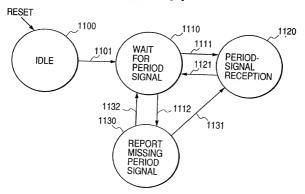


FIG. 11



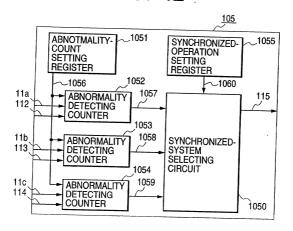


FIG. 13

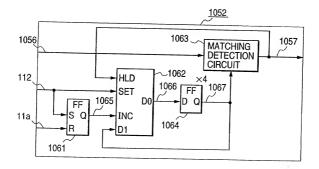
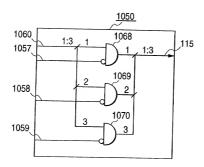


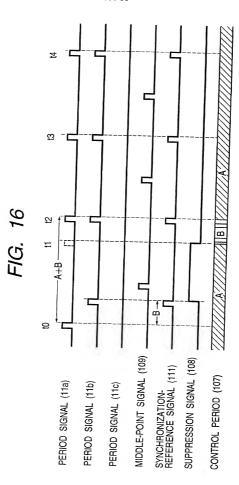
FIG. 14

SET	INC	HLD	DO
0	0	0	DI
0	0	1	DI
0	1	0	DI
0	1	1	DI
1	0	0	1
1	0	1	DI
1	1	0	DI+1
1	1	1	DI

FIG. 15









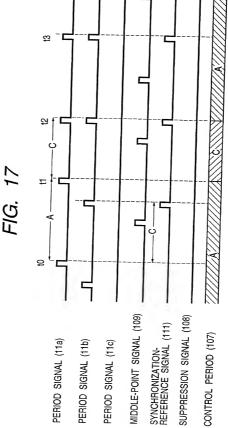
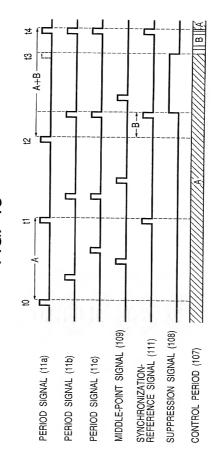
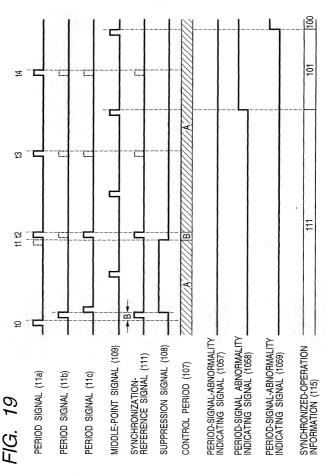
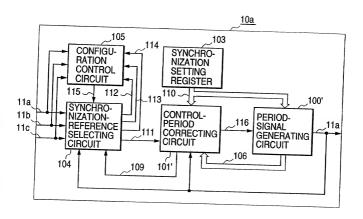


FIG. 18



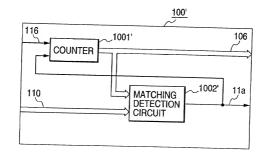






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FIG. 21



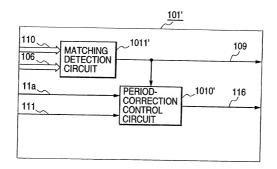


FIG. 23

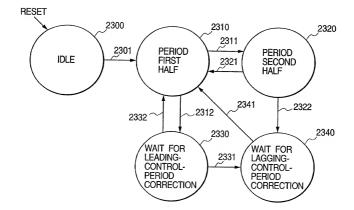


FIG. 24

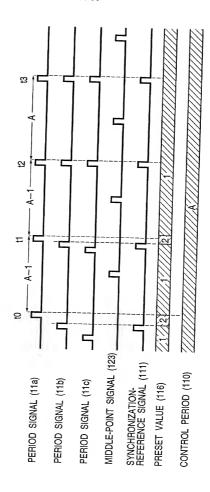


FIG. 25

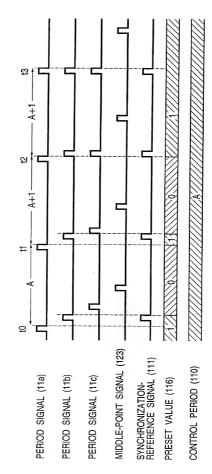
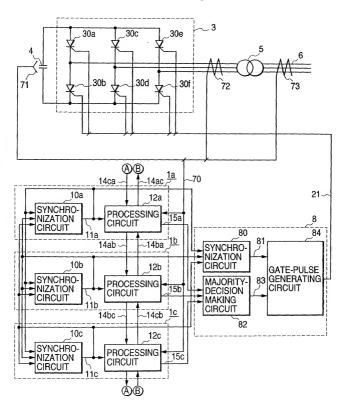
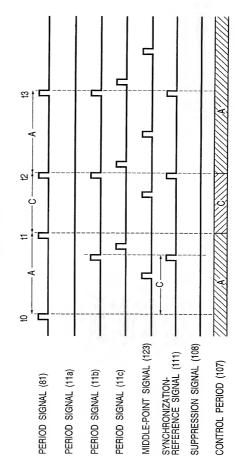


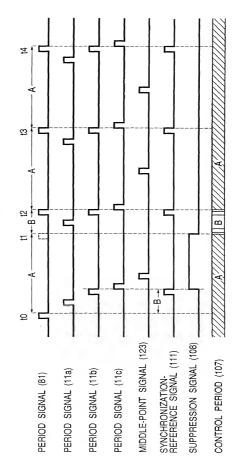
FIG. 26











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FIG. 29

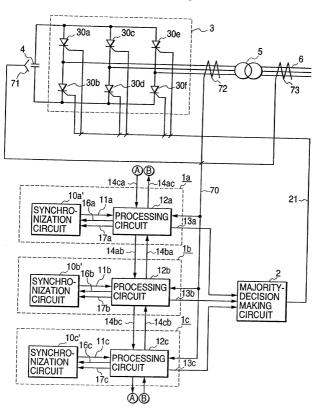


FIG. 30

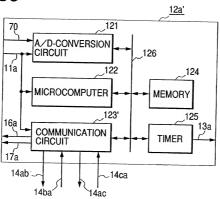


FIG. 31

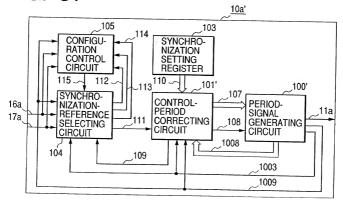


FIG. 32

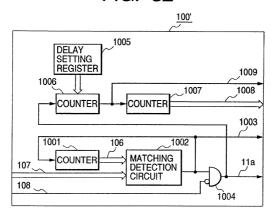


FIG. 33

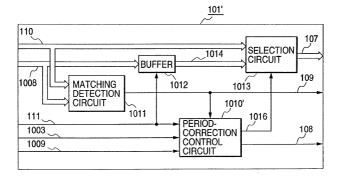


FIG. 34

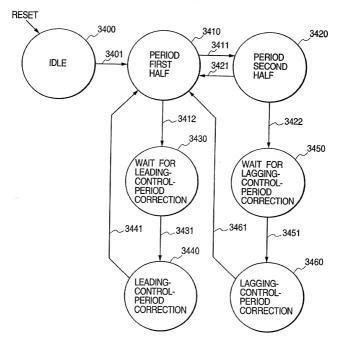
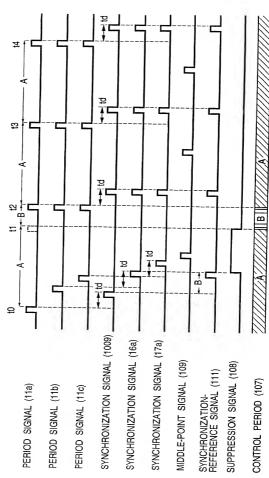


FIG. 35





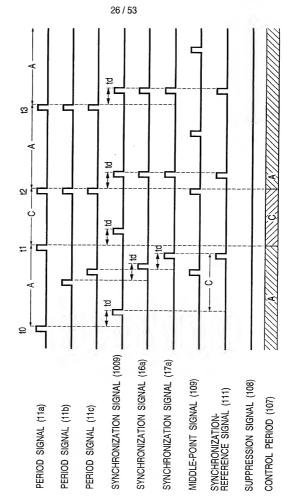
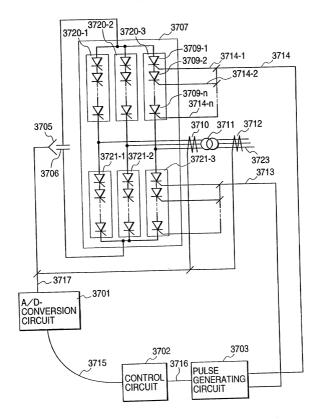


FIG. 37



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FIG. 38

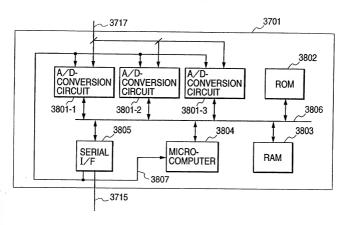


FIG. 39

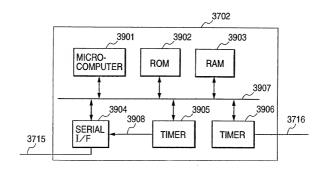
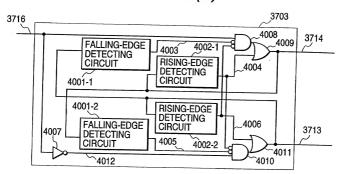


FIG. 40(a)



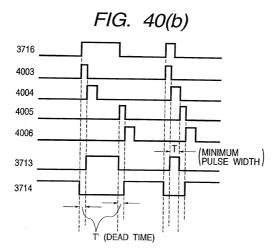


FIG. 41(a)

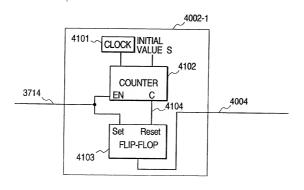
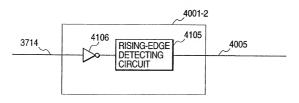
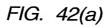


FIG. 41(b)





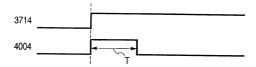


FIG. 42(b)

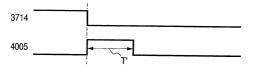


FIG. 43

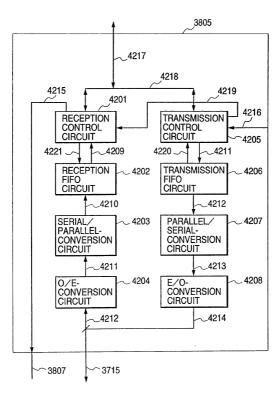


FIG. 44

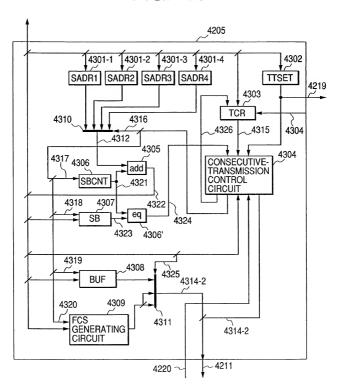
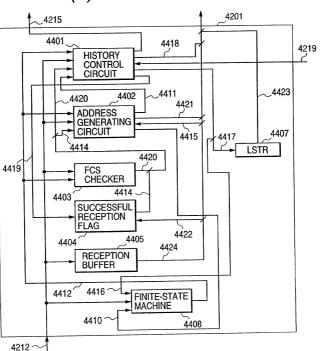
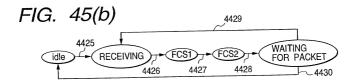


FIG. 45(a)





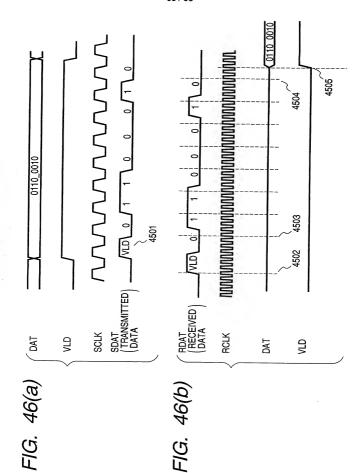


FIG. 47(a)

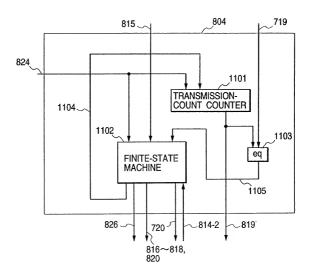


FIG. 47(a)

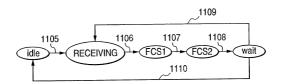


FIG. 48

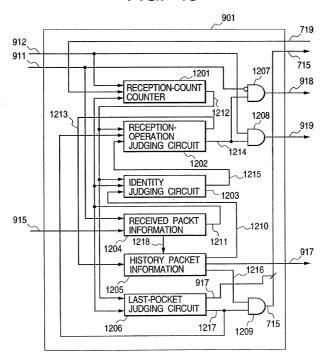


FIG. 49

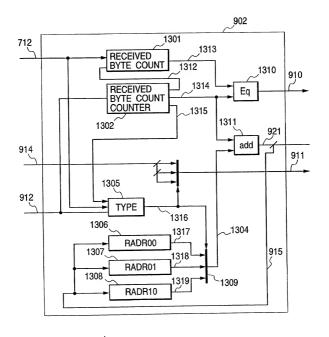


FIG. 50(a)

TYPE	~ ¹⁴⁰¹	_1402					
\$0010 0000_0000	0000_0000	0001_0000					
\$0011 0000_0110	0000_0110	0000_0110					
\$0012 0110_1001	0110_1001	0110_1001					
\$0013 0100_1000	0100_1000	0100_1000					
\$0014 0010_0010	0010_0010	0010_0010					
\$0015 1001_0100	1001_0100	1001_0100					
\$0010 : TYPE	FCS1 1101_0100	FCS1 1001_0110					
\$0011 : TRANSMITTED BYTE	FCS2 0011_1000	FCS2 0110_0100					
COUNT : 6 (PACKET SIZE)							
\$0012~\$0015: USER DATA							
PACKET DATA ON THE RAM	CONSECUTIVE TRANSMISSION FIRST PACKET	CONSECUTIVE TRANSMISSION SECOND PACKET					

FIG. 50(b)

TYPE	1403	1404
\$0110 (11)00_0000	1100_0000	1101_0000
\$0111 0000_0010	0000_0110	0000_0110
\$0010 : TYPE	FCS1 0001_0101	FCS1 0011_1010
\$0011 : TRANSMITTED BYTE COUNT : 2 (PACKET SIZE) (NO USER-DATA AREA)	FCS2 1011_0100	FCS2 1101_0101
PACKET DATA ON THE RAM	CONSECUTIVE TRANSMISSION FIRST PACKET	CONSECUTIVE TRANSMISSION SECOND PACKET

FIG. 51(a)

RECEPTION-OPERATION JUDGING CIRCUIT 4702 REQUEST FOR A MEMORY WRITE: NO SUCCESSFUL HISTORY PACKET OF THE SAME TYPE IS RECEIVED IN THE PAST DURING RECEPTION OF DATA PACKETS. REQUEST TO SET THE SUCCESSFUL-RECEPTION FLAG 4404: NO ERROR RECORD EXISTS IN THE RECEIVED-PACKET-INFORMATION REGISTER 4704 AND (THE RECEPTION COUNT IN THE RECEPTION-COUNT COUNTER 4701=0 OR A RECORD OF AN ERROR EXISTS IN THE HISTORY-PACKET-INFORMATION REGISTER 4705 OR THE IDENTITY JUDGING CIRCUIT 4703 JUDGES A PACKET CURRENTLY BEING RECEIVED TO BE A PACKET OF A DIFFERENT TYPE). ERROR-LOG REQUEST: INFORMATION INDICATING DETECTION OF AN ERROR EXISTS IN THE HISTORY-PACKET-INFORMATION REGISTER 4705 AND A PACKET CURRENTLY BEING RECEIVED IS THE LAST ONE. INTERRUPT SIGNAL TO THE SIGNAL LINE 4415: THE TYPE OF A PACKET STORED IN THE HISTORY-PACKET-INFORMATION REGISTER 4705 IS INDICATED BY A CODE OF '11' AND NO RECORD OF AN ERROR EXISTS IN THE HISTORY-PACKET-INFORMATION REGISTER 4705 AND A PACKET CURRENTLY BEING RECEIVED IS THE LAST ONE.

FIG. 51(b)

IDENTITY JUDGING CIRCUIT 4703:

NO ERROR EXISTS IN A PACKET CURRENTLY BEING RECEIVED AND THE RECEPTION COUNT IN THE RECEPTION-COUNT COUNTER 4701 IS NOT 0 AND (NO ERROR HISTORY EXISTS IN THE HISTORY-PACKET-INFORMATION REGISTER 4705 AND (THE PACKET TYPE RECORDED IN THE RECEIVED-PACKET-INFORMATION REGISTER 4704)—(THE PACKET TYPE RECORDED IN THE HISTORY-PACKET-INFORMATION REGISTER 4705) AND THE RECEPTION COUNT IN THE RECEPTION-COUNT COUNTER 4701</Td>
 THE SEQUENCE NUMBER n OF THE REPEATED TRANSMISSION OF THE PACKET CURRENTLY BEING RECEIVED) OR (AN ERROR HISTORY EXISTS IN THE HISTORY-PACKET-INFORMATION REGISTER 4705 AND THE RECEPTION COUNT IN THE RECEPTION-COUNT COUNTER 4701
 THE SEQUENCE NUMBER n OF THE RECEPTION-COUNT COUNTER 4701
 THE SEQUENCE NUMBER n OF THE REPEATED TRANSMISSION OF THE PACKET CURRENTLY BEING RECEIVED)

FIG. 51(c)

RECEIVED-PACKET-INFORMATION REGISTER 4704 HAVING A WIDTH OF 6 BITS:

PACKET TYPE:2 BITS

THE SEQUENCE NUMBER n OF THE REPEATED TRANSMISSION: 2 BITS ERROR INFORMATION-2 BITS:

ERROR TYPE: 1 BIT INDICATING A CRC ERROR OR A RECEIVED-DATA OVERBUN ERROR EDDOD DETECTION: 1 BIT INDICATING WHETHER AN EDDOD IS

ERROR DETECTION: 1 BIT INDICATING WHETHER AN ERROR IS DETECTED OR NOT

FIG. 51(d)

HISTORY-PACKET-INFORMATION REGISTER 4705 HAVING A WIDTH OF 4 BITS:

PACKET TYPE: 2 BITS

ERROR INFORMATION-2 BITS:

ERROR TYPE: 1 BIT INDICATING A CRC ERROR OR A RECEIVED-DATA OVERRUN ERROR

ERROR DETECTION: 1 BIT INDICATING WHETHER AN ERROR IS

FIG. 51(e)

LAST-PACKET JUDGING CIRCUIT 4706:

(NO ERROR IS DETECTED IN A PACKET CURRENTLY BEING RECEIVED AND THE CONTENTS OF THE REPEATED-TRANSMISSION-COUNT SETTING REGISTER 4302/<=THE SEQUENCE NUMBER n OF THE REPEATED TRANSMISSION OF THE PACKET CURRENTLY BEING RECEIVED) AND (AN ERROR IS DETECTED IN A PACKET CURRENTLY BEING RECEIVED AND THE CONTENTS OF THE REPEATED-TRANSMISSION-COUNT SETTING REGISTER 4302<=THE CONTENTS OF THE RECEPTION-COUNTER 4701) OR (A SUCCESSFUL-RECEPTION HISTORY EXISTS IN THE HISTORY-PACKET-INFORMATION REGISTER 4705 AND THE PACKET TYPE STORED IN THE RECEIVED-PACKET-INFORMATION REGISTER 4704 IS DIFFERENT FROM THE PACKET TYPE STORED IN THE HISTORY-PACKET-INFORMATION REGISTER 4705) OR A TIME-OUT EVENT OCCURS DURING A PACKET WAITING STATE OF THE FINITE STATE MACHINE 4408.

FIG. 52

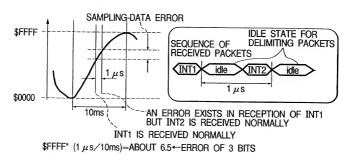


FIG. 54

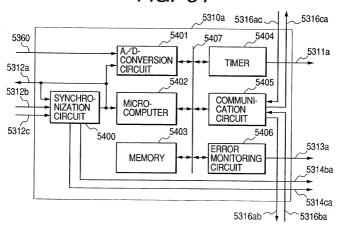
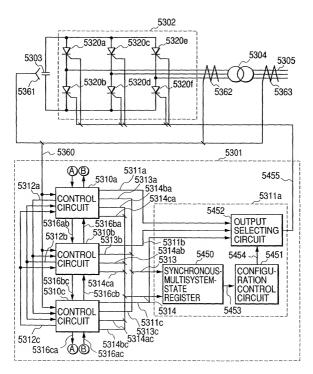
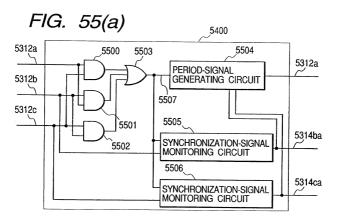
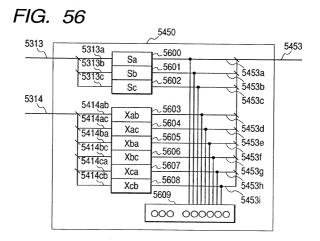


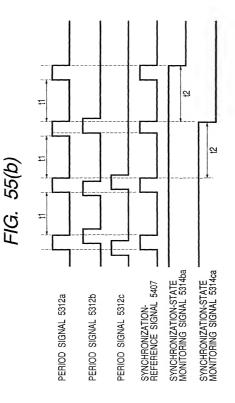
FIG. 53











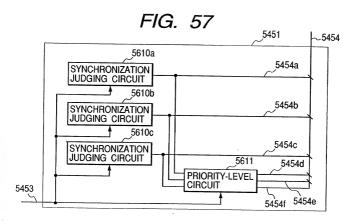
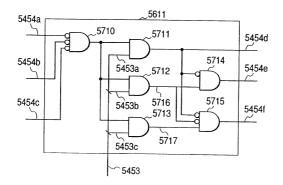


FIG. 59



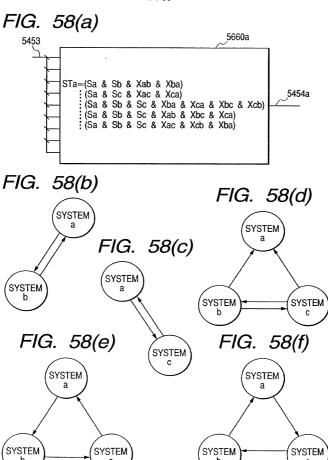
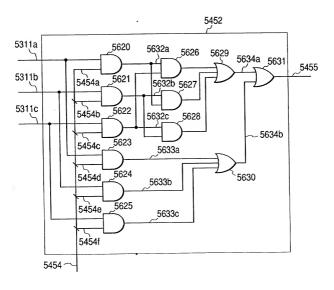


FIG. 60



ANNOSSOS

FIG. 61

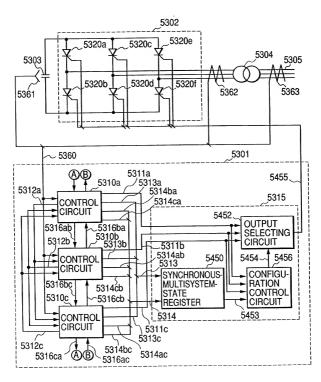


FIG. 62

14

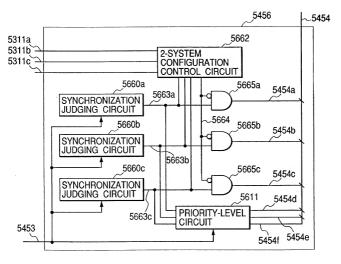


FIG. 63(a)

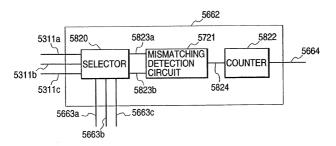


FIG. 63(b)

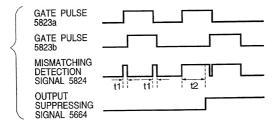


FIG. 64(a)

SELECTOR 5820

5663a	5663b	5663c	5823a	5823b
0	0	0	0	0
0	0	1	0	0
0	1	0	0	0
0	1	1	11b	11c
1	0	0	0	0
1	0	1	11a	11c
1	1	0	11a	11b
1	1	1	0	0

FIG. 64(b)

MISMATCHING DETECTION CIRCUIT 5821

	5823a	5823b	5824
j	0	0	0
	0	1	1
	1	0	1
ı	1	1	0

_m.

FIG. 65

